

Practice for Final Exam

Student **FULL** Name: _____

Lab Section: _____

Write your FULL name (the same as how you put in the system) as CLEAR as possible!

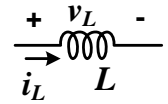
You are allowed two 8.5" × 11" sheets of prepared notes, a pen or pencil, and a calculator. No internet connected devices are allowed. Write legibly. For maximum credit, state all assumptions and show all work in a clear manner.

Problem #	Points Possible	Points Earned
1	8	
2	22	
3	10	
4	15	
5	22	
6	18	
Submission	5	

Problem 1: Circle the correct answer (8 pts, 2 pts x 4)

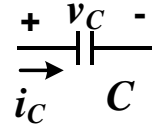
a) For the inductor defined on the right, which equation is correct:

A. $i_L = L \frac{dv_L}{dt}$ **B.** $v_L = L \frac{di_L}{dt}$



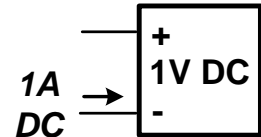
b) For the capacitor defined on the right, which equation is correct:

A. **$i_C = C \frac{dv_C}{dt}$** B. $v_C = C \frac{di_C}{dt}$

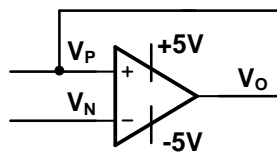


c) For the black box on the right with given current and voltage, the box is:

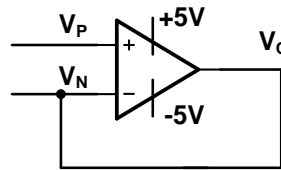
A. Developing power B. Absorbing power



d) Which of the circuits below represents negative feedback and more likely to be used for linear operation?

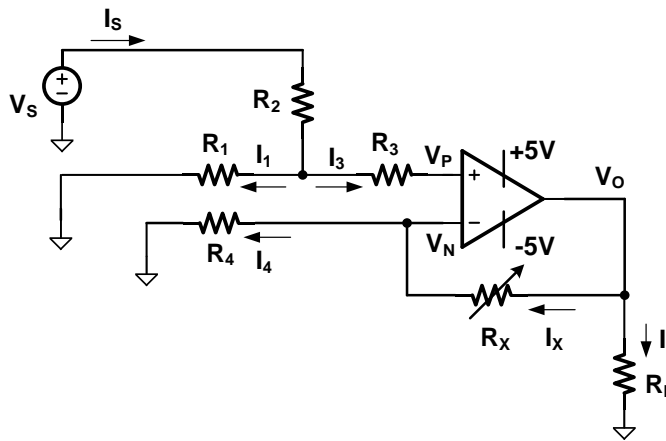


A



B

Problem 2: Op-amp circuits (22 pts)



For the above circuits, assume ideal Opamp and linear operation. R_x is a variable resistor and can be adjusted. All elements: V_S , R_1 , R_2 , R_3 , R_4 , R_L , R_x , have known values. The supply voltages for the opamp are 5V and -5V.

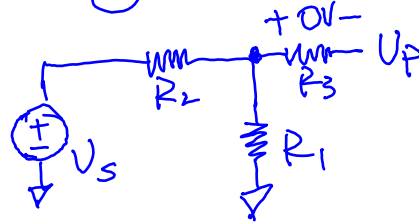
a) How much is I_3 ? [1 pt] $I_3 = 0$

b) Will R_L affects the V_O (Note that the Opamp is ideal)? [1 pt]

No. With ideal opamp, the only difference will be the current sourced/sinked by the opamp.

c) Find the voltage equation for V_P (you don't need to plug in values yet). [3 pts]

V_P can be calculated by the voltage divider:



$$V_P = V_S \cdot \frac{R_1}{R_1 + R_2}$$

d) Find the gain equation for $A_v = V_o/V_s$. [10 pts]

$$V_N = V_P = V_S \frac{R_1}{R_1 + R_2}$$

$$\frac{V_N - 0}{R_4} + \frac{V_N - V_o}{R_x} = 0$$

$$\Rightarrow R_x V_N + R_4 V_N = V_o R_4$$

$$\Rightarrow (R_x + R_4) \frac{R_1 V_S}{R_1 + R_2} = V_o R_4$$

$$\frac{V_o}{V_S} = \frac{R_x + R_4}{R_4} \cdot \frac{R_1}{R_1 + R_2} \quad \times$$

or: Non-inverting:

$$V_o = V_P \cdot \left(1 + \frac{R_x}{R_4}\right)$$

$$= V_S \frac{R_1}{R_1 + R_2} \cdot \left(1 + \frac{R_x}{R_4}\right)$$

$$\Rightarrow \frac{V_o}{V_S} = \frac{R_1}{R_1 + R_2} \cdot \left(1 + \frac{R_x}{R_4}\right) \quad \times$$

e) The parameters have values of: $V_S=2V$, $R_1=R_2=2k\Omega$, $R_3=R_4=1k\Omega$, $R_x=0\Omega$. Calculate V_o . [2 pts]

$$V_o = V_S \cdot \frac{R_1}{R_1 + R_2} \cdot \left(1 + \frac{R_x}{R_4}\right)$$

$$= 2 \times \frac{2k}{4k} \cdot \left(1 + \frac{0}{1k}\right) = 1V \quad \times$$

f) With other parameters the same as in e), adjust R_x and find the maximum possible R_x while maintaining linear operation for the Opamp. [5 pts]

For linear operation: $-5 \leq V_o \leq 5V$.

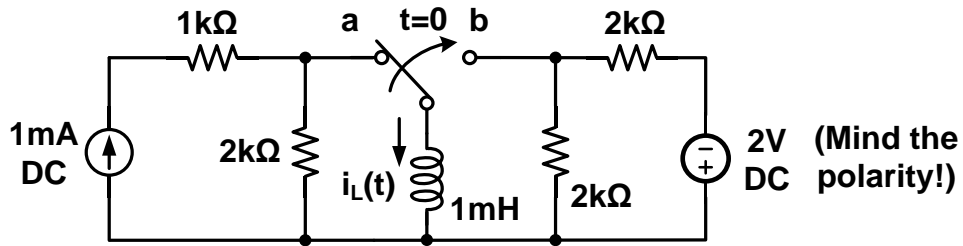
$$-5 \leq 2 \times \frac{2k}{4k} \times \left(1 + \frac{R_x}{1k}\right) \leq 5$$

$$-5 \leq 1 + \frac{R_x}{1k} \leq 5$$

$$-6k \leq R_x \leq 4k, \quad R_x \geq 0$$

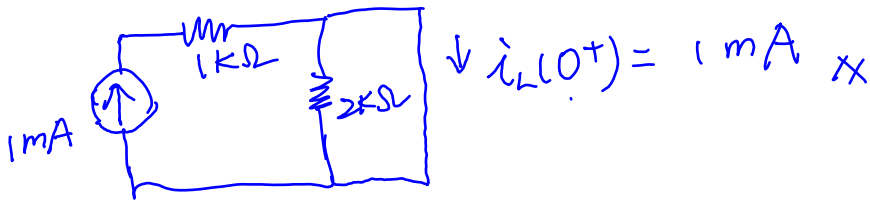
$$\therefore R_x \leq 4k\Omega \quad \times$$

Problem 3 RL Response: (10 pts)

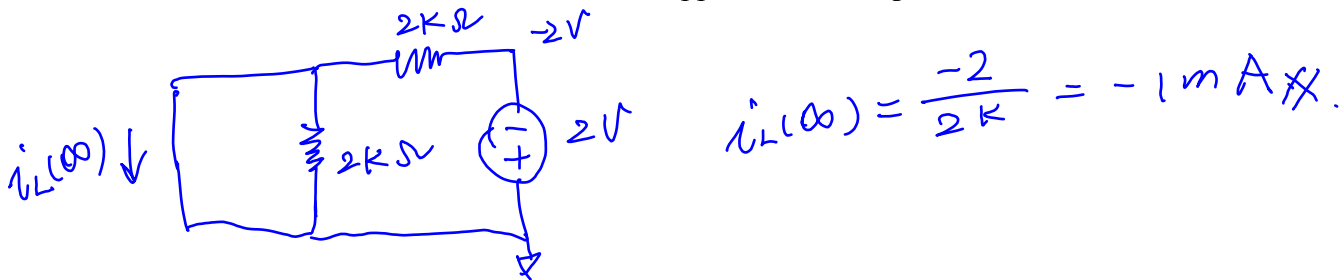


The switch stays at position “a” for a very long time. At $t=0$, it switches to “b”.

- a) Find the initial condition of $i_L(0^+)$. [2 pts]

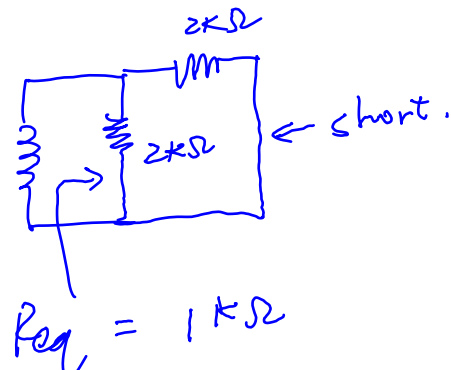


- b) Find the final condition of $i_L(t)$ when t approaches ∞ . [2 pts]



- c) Find the time constant τ of the circuit for $t \geq 0^+$. [2 pts]

$\tau = \frac{L}{R_{eq}} = \frac{1\text{m}}{1\text{k}} = 1\mu\text{s}$



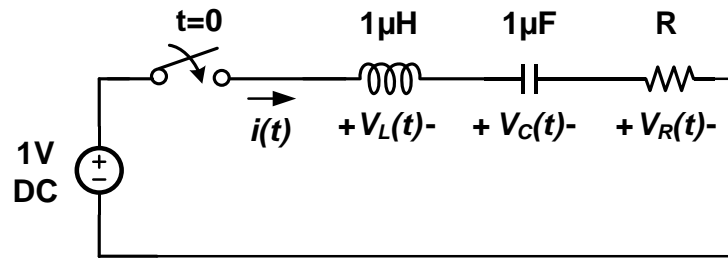
- d) Find the full equation of $i_L(t)$ for $t \geq 0$. [4 pts]

$$i_L(t) = I_F + (I_0 - I_F)e^{-\frac{t}{\tau}}$$

$$= -1\text{m} + (1\text{m} + 1\text{m})e^{-\frac{t}{1\mu}}$$

$$= -(1\text{m} + 2\text{m})e^{-\frac{t}{1\mu}}, \quad t \geq 0$$

Problem 4: RLC circuits (15 pts)



The initial energy stored in the inductor and capacitor is zero. The switch was open before $t=0$ for a long time. At $t = 0$, the switch closes and a 1V DC voltage source is attached to the circuit.

We know for series RLC circuit: $\alpha = \frac{R}{2L}$, $\omega_0 = \frac{1}{\sqrt{LC}}$.

- a) Find the R value for critical damping. (5 pts)

critical damping $\Rightarrow \alpha = \frac{R}{2L} = \omega_0 = \frac{1}{\sqrt{LC}}$
 $\Rightarrow R = \frac{2\sqrt{L}}{\sqrt{C}} = 2 \times \frac{\sqrt{1\mu}}{\sqrt{1\mu}} = 2\Omega \neq$

- b) What will be initial voltage of $V_L(0^+)$, $V_C(0^+)$ and $V_R(0^+)$, immediately after the switch closes (6 pts)

c) No initial energy for both L & C :
 $i(0^+) = 0A, V_C(0^+) = 0V$

At $t=0^+$: KVL: $V_L(0^+) + V_C(0^+) + V_R(0^+) = 1$
 $\therefore V_L(0^+) = 1V \neq$
 $1V \quad 0 \quad i(0^+) \cdot R = 0$

- d) If R is larger than the value calculated in a), the circuit will be in (Circle the correct answer): (2 pts):

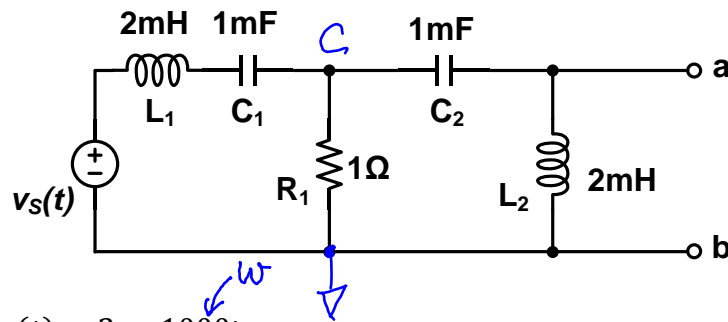
- A. Under-damped condition;
 B. Over-damped condition;
 C. Critical-damped condition.

$\uparrow \alpha = \frac{R}{2L}$
 if R increases, α increases,
 $\alpha > \omega_0$

- e) If R is larger than the value calculated in a), (Circle the correct answer) (2 pts):

- A. Ringing will happen;
 B. The response will still be smooth but will settle slower, without ringing.
 C. The response will still be smooth but will settle faster, without ringing.

Problem 5: Frequency Domain Circuit Analysis (22 pts)



In the above circuits, $v_s(t) = 2\cos 1000t$.

a) Convert the inductors and capacitors into impedance. (6 pts)

$$Z_{L1} = j\omega L_1 = j \times (1k \times 2m) = j2\Omega = Z_{L2} = Z_L \quad \times$$

$$Z_{C1} = -\frac{j}{\omega C} = -\frac{j}{1k \times 1m} = -j\Omega = Z_{C2} = Z_C \quad \times$$

b) Find the Thevenin Equivalent voltage \rightarrow in the frequency domain, then convert it back to the time domain $V_{TH}(t)$. (8 pts)

$$\vec{V}_s = 2\angle 0^\circ = 2V$$

$$\Rightarrow \frac{V_c - 2}{j2 - j1} + \frac{V_c}{1} + \frac{V_c}{-j + j2} = 0$$

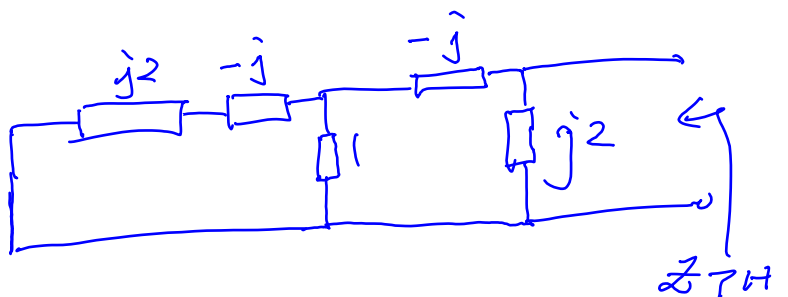
$$\Rightarrow \frac{V_c - 2}{j} + V_c + \frac{V_c}{j} = 0$$

$$V_c = 0.8 - j0.4$$

$$V_{TH} = V_a = V_c \times \frac{j2}{j2 - j} = 1.6 - j0.8 \quad \times$$

$$V_{TH}(t) = 1.79 \cos(1000t - 26.6^\circ) \quad \times$$

c) Find the Thevenin Equivalent impedance Z_{TH} . (5 pts)



$$Z_{TH} = [(j \parallel 1) - j] \parallel j2 = [(0.5 + j0.5) - j] \parallel j2$$

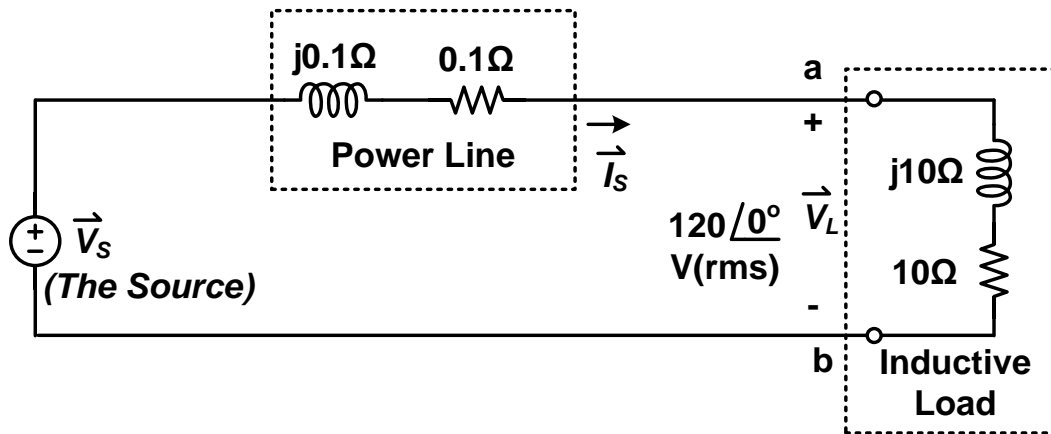
$$= (0.5 - j0.5) \parallel j2 = 0.8 - j0.4 (\Omega) \quad \times$$

- d) In order to extract the maximum real power from the source, find the loading impedance that should be attached between “a” and “b”. (3 pts)

$$Z_L = Z_{Th}^* = 0.8 + j0.4 \Omega$$

for maximum real power.

Problem 6: Complex Power (18 pts)



In the above circuit, an inductive load is attached between terminals “a” and “b”. The power line also has finite impedance. The loading now sees an AC voltage V_L of 120V rms with 0° phase angle. (All the voltages and currents in this problem are in **rms** or **effective** values.)

- a) Find the current phasor \vec{I}_s . (2 pts)

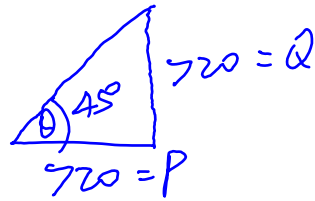
$$\vec{V}_L = 120 \text{ V}, \quad \vec{I}_s = \frac{\vec{V}_L}{Z_L} = \frac{120}{10 + j10} = 6 - j6 \text{ A.}$$

- b) Find the complex power (including average power and reactive power) and power factor of the Load. (6 pts)

$$S_L = \vec{V}_L \cdot \vec{I}^* = 120 \times (6 + j6)$$

(rms) $= 720 + j720 \text{ VA}$ ✗

PF:



$$PF = \cos \theta = 0.707 \left(= \frac{720}{\sqrt{720^2 + 720^2}} \right) \text{ ✗}$$

- c) Find the average power dissipated by the power line. (4 pts)

$$S_{\text{line}} = |\vec{I}_s|^2 \cdot Z_L = |\vec{I}_s|^2 \cdot (0.1 + j0.1)$$

$$P_{\text{line}} = (\sqrt{6^2 + 6^2})^2 \cdot 0.1$$

$$= 7.2 \text{ W} \text{ ✗}$$

- d) Find the complex power delivered by the source. (6 pts)

$$S_s = \vec{V}_s \cdot \vec{I}_s^*, \quad \vec{V}_s = \vec{V}_L + \vec{V}_{\text{line}}$$

$$= 120 + \vec{I}_s \cdot Z_{\text{line}}$$

$$= 120 + (6 - j6)(0.1 + j0.1)$$

$$= 121.2 \text{ V} \cdot \text{A} \text{ ✗}$$

$$S_s = 121.2 \times (6 + j6)$$

$$= 727.2 + j727.2 \text{ ✗}$$

or:

$$S_s = S_L + S_{\text{line}}$$

$$= 720 + j720$$

$$+ 7.2 + j7.2$$

$$= 727.2 + j727.2 \text{ ✗}$$